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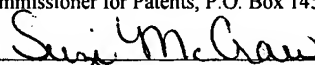
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**MCDERMOTT, WILL & EMERY**

November 25, 2003

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I hereby certify that this correspondence and identified enclosures are being deposited on November 25, 2003, with the U.S. Postal Service, "Express Mail Post Office to Address" service, No. EV313252902US, under 37 C.F.R. § 1.10, addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Suzi McCraw

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: Client: Qualcomm, Incorporated

Matter No. 050278-094

U.S. Patent Application

Title: CALIBRATING AN INTEGRATED CIRCUIT TO AN  
ELECTRONIC DEVICE

Inventor: Jagrut Patel, et al.

Subject: Transmitting Patent Application for Filing

Dear Sir or Madam:

We enclose for filing the patent application of:

Inventor: Jagrut Patel, et al.

For: CALIBRATING AN INTEGRATED CIRCUIT TO AN ELECTRONIC  
DEVICE

This applicant claims Large Entity Status. See 37 C.F.R. 1.27.

The application includes:

- Cover + 19 pages of specification, claims, and abstract
- Six (6) sheet(s) of drawings (Figures 1-6)

Mail Stop Patent Application  
Commissioner for Patents  
November 25, 2003  
Page 2

Please stamp the filing date on the enclosed postcard and return it to us.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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**Date: November 25, 2003**